



APPROVAL SHEET

Approval Specification	Customer's Approval Certificate
<p>TO:</p> <p>Part No.:</p> <p>Customer's Part No.:</p>	<p>Please return this copy as a certification of your approval</p> <p>Checked & Approved by:</p> <p>Date:</p>

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Part No.	:	SF0592
Pages	:	6
Date	:	2015/7/15
Revision	:	1.0

Prepared by:	梁浩
Checked by:	
Approved by:	

Application

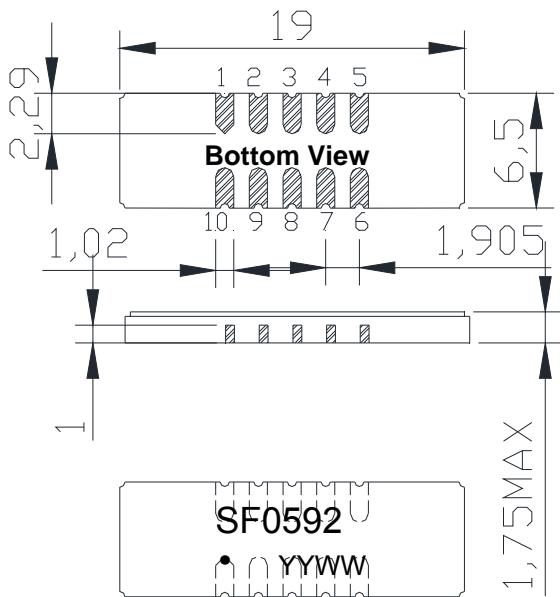
- High-loss SAW component
- Low amplitude ripple
- Sharp rejections at both out-bands
- Passband 1 MHz

Features

- Ceramic Package for **Surface Mounted Technology (SMT)**
- **RoHS** compatible
- Package size 19.00x6.50x1.75mm³
- Package Code SMD19

- **Electrostatic Sensitive Device(ESD)**

Package Dimensions (Unit: mm)



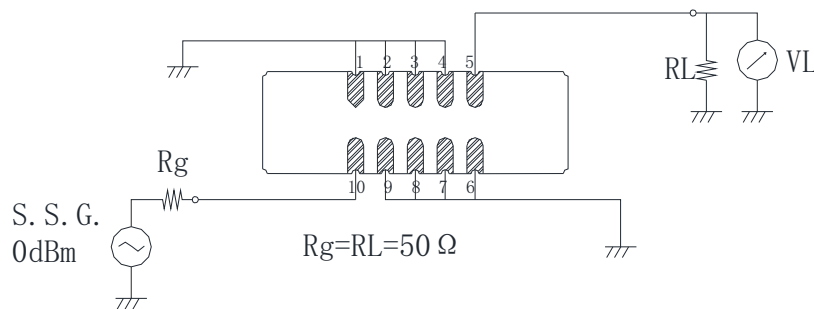
Pin Configuration

Pin No.	Description
10	Input
5	Output
1,2,3,4,6,7,8,9	Ground

Marking Description

S	Trademark
F	SAW Filter
0592	Part Number
●	Pin 1
YYWW	Year Code & Week Code

Test Circuit(Bottom View)



*Fig: If the products produced in 06th week of 2012, The year code & week code is 1206.

Performance**Maximum Rating**

Item		Value	Unit
DC Voltage	V _{DC}	3	V
Operation Temperature	T	-40 ~ +85	°C
Storage Temperature	T _{stg}	-55 ~ +125	°C
RF Power Dissipation	P	10	dBm

Electronic Characteristics

Test Temperature: 25°C ± 2°C

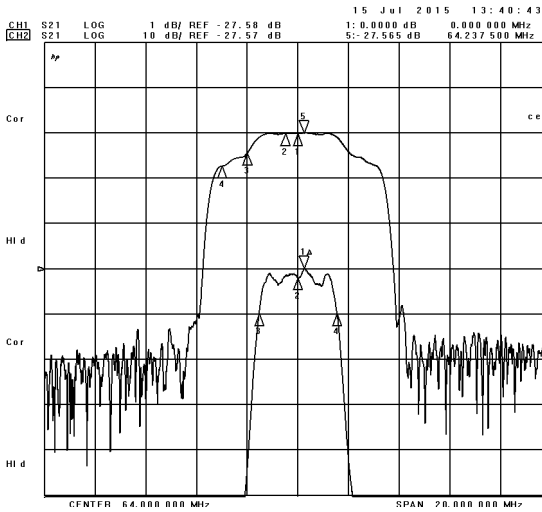
Terminating source impedance: 50Ω

Terminating load impedance: 50Ω

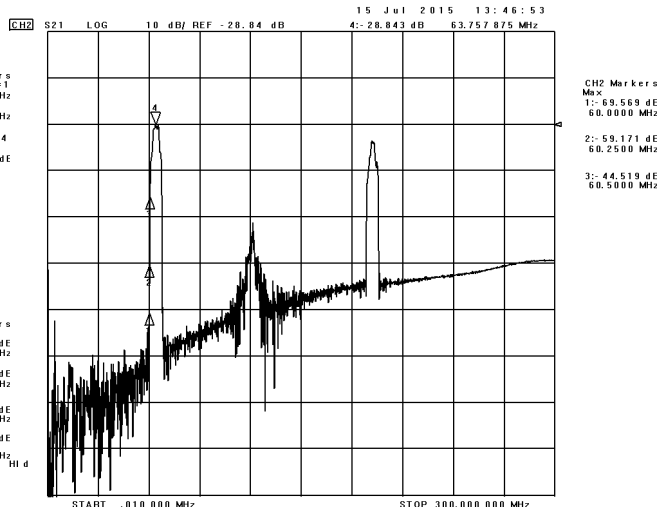
Item		Minimum	Typical	Maximum	Unit
Center Frequency	f _c		64.0		MHz
Insertion Loss(min)	IL		27.6	31.0	dB
Absolute Attenuation	α				
	60.00MHz	40.0	41.0		dB
	60.25MHz	27.0	31.0		dB
	60.50MHz	12.0	15.0		dB
	61.00MHz	5.0	7.0		dB
	62.00MHz	2.0	4.0		dB
	63.50MHz	0.0	0.1		dB
	64.00MHz	0.0	0.1		dB

Frequency Characteristics

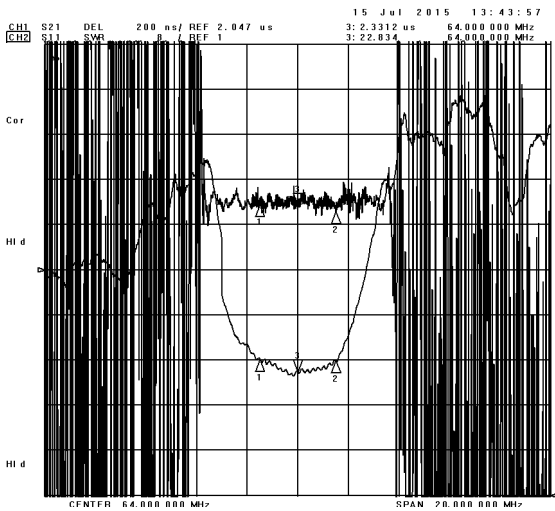
Frequency Response



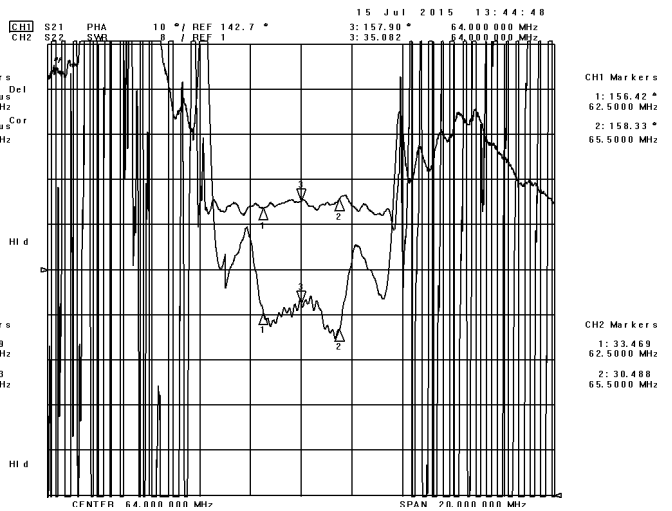
Frequency Response (wideband)



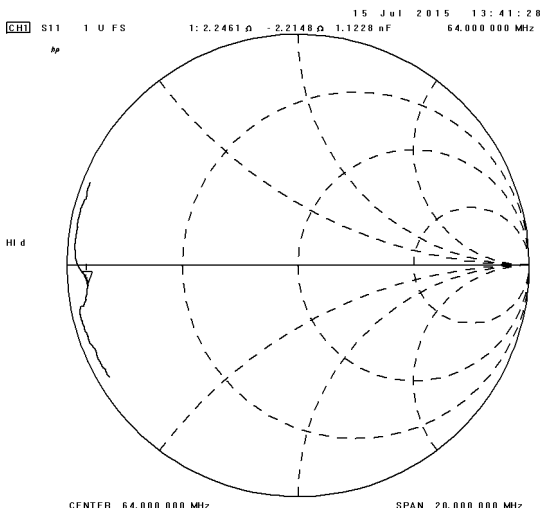
Delay Ripple & S11 VSWR



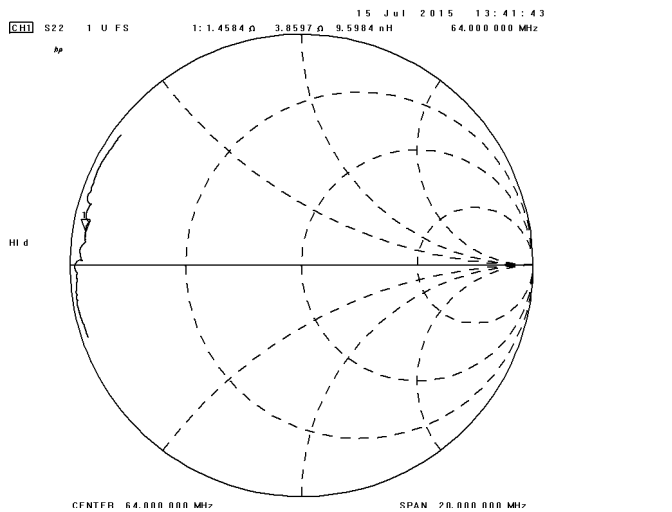
Phase Linearity & S22 VSWR



S11 Smith Chart



S22 Smith Chart



Notes

1. As a result of the particularity of inner structure of SAW products, it easy to be breakdown by electrostatic, so we should pay attention to **ESD protect** in the test.
2. **Static voltage** between signal load and ground may cause deterioration and destruction of the component. Please avoid static voltage.
3. **Ultrasonic cleaning** may cause deterioration and destruction of the component. Please avoid ultrasonic cleaning.
4. Only leads of component may **be soldered**. Please avoid soldering another part of component.
5. There is a close relationship between the device's performance and **matching network**. The specifications of this device are based on the test circuit shown above. L and C values may change depending on board layout. Values shown are intended as a guide only.