



APPROVAL SHEET

Approval Specification	Customer's Approval Certificate
<p>TO:</p> <p>Part No.:</p> <p>Customer's Part No.:</p>	<p>Please return this copy as a certification of your approval</p> <p>Checked & Approved by:</p> <p>Date:</p>

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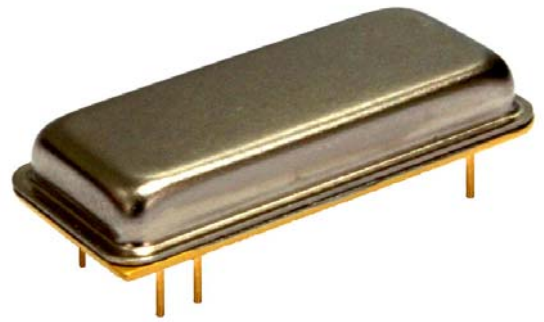


Part No.	:	SF1220
Pages	:	6
Date	:	2013/2/21
Revision	:	1.1

Prepared by:	梁浩
Checked by:	
Approved by:	

Application

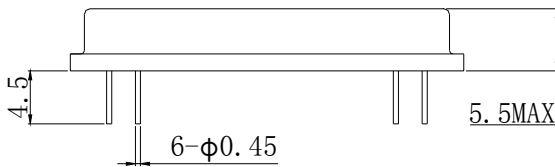
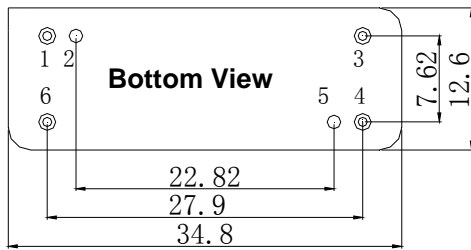
- Low amplitude ripple
- Sharp rejections at both out-bands
- Usable passband 200 KHz



Features

- RoHS compatible
- Package size 27.2x12.64x7.00mm³
- Package Code DIP2712J
- Electrostatic Sensitive Device(ESD)

Package Dimensions (Unit: mm)



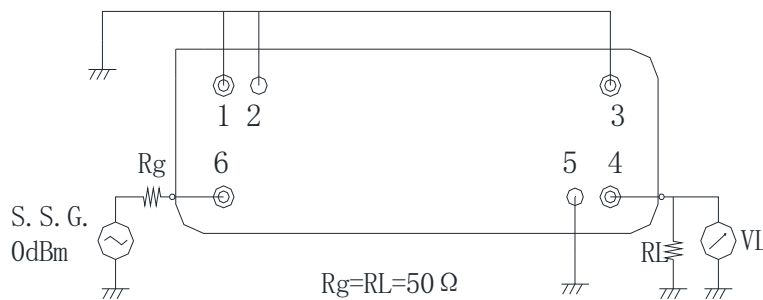
Pin Configuration

Pin No.	Description
6	Input
4	Output
1,2,3,5	Ground

Marking Description

S	Trademark
F	SAW Filter
1220	Part Number
●	Pin 1
YYWW	Year Code & Week Code

Test Circuit(Bottom View)



Performance**Maximum Rating**

Item		Value	Unit
DC Voltage	V _{DC}	3	V
Operation Temperature	T	-40 ~ +85	°C
Storage Temperature	T _{stg}	-55 ~ +125	°C
RF Power Dissipation	P	10	dBm

Electronic Characteristics

Test Temperature: 25°C ± 2°C

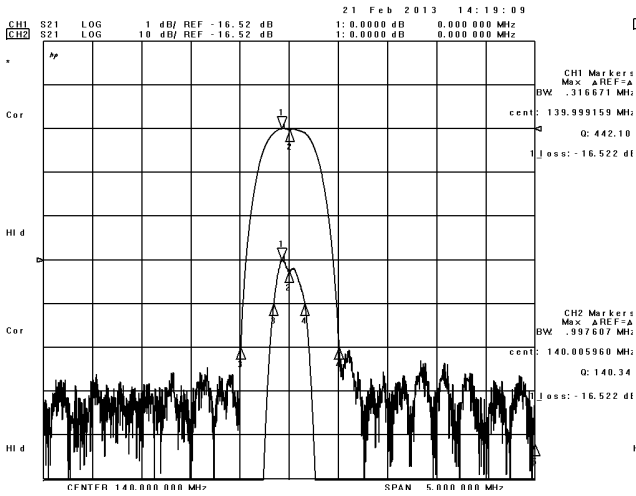
Terminating source impedance: 50Ω

Terminating load impedance: 50Ω

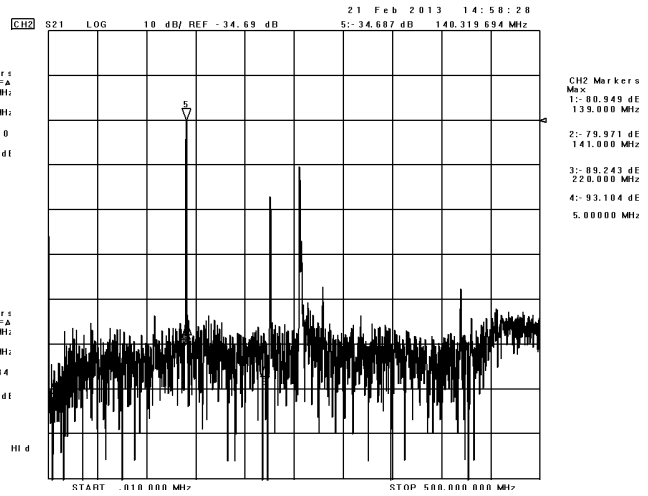
Item		Minimum	Typical	Maximum	Unit
Center Frequency	f _c	139.90	140.00	140.10	MHz
Insertion Loss(min)	IL		16.5	21.0	dB
Amplitude Ripple (p-p) 139.90-140.10MHz	Δa		0.6	1.0	dB
0.6 dB Bandwidth	BW _{0.6dB}	200.0	250.0		KHz
1 dB Bandwidth	BW _{1dB}	300.0	310.0		KHz
50 dB Bandwidth	BW _{50dB}		1000.0	1100.0	KHz
Phase Linearity 139.90-140.10MHz			4.0	5.0	deg
Absolute Delay 140.00MHz			4.0	4.5	us
Absolute Delay					
Absolute Attenuation	a				
5.00 -139.00 MHz		52.0	53.0		dB
134.90MHz		63.0	65.0		dB
138.90MHz		55.0	58.0		dB
139.30MHz		45.0	48.0		dB
139.50MHz		40.0	43.0		dB
140.50MHz		35.0	40.0		dB
140.70MHz		45.0	55.0		dB
141.10MHz		55.0	58.0		dB
145.10MHz		60.0	63.0		dB
141.00-220.00MHz		50.0	53.0		dB

Frequency Characteristics

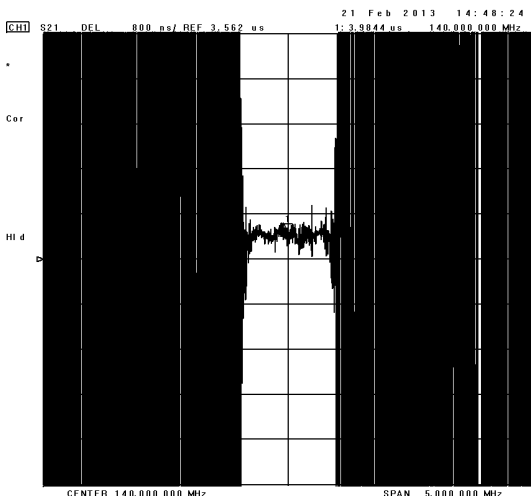
Frequency Response



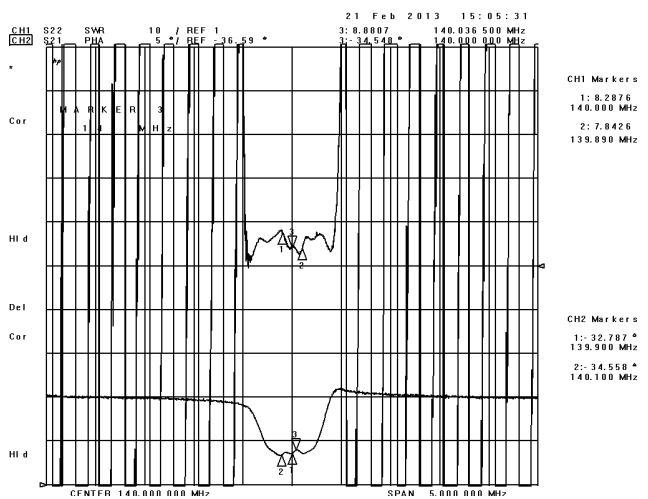
Frequency Response (wideband)



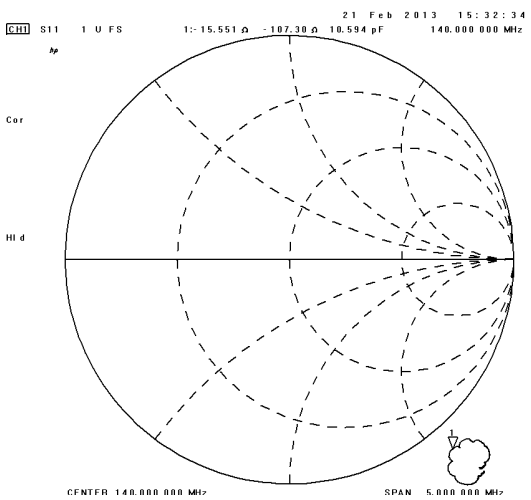
Delay Ripple



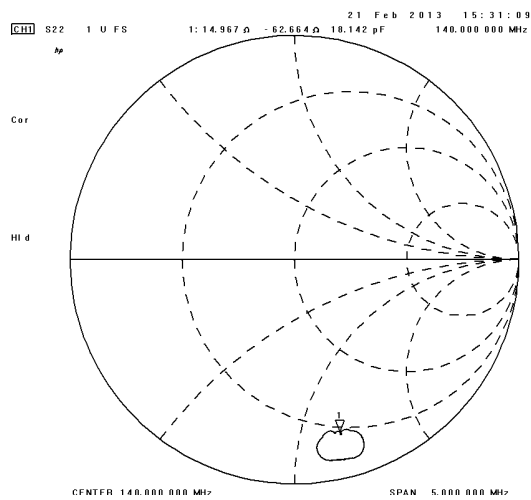
Phase Linearity & S22 VSWR



S11 Smith Chart



S22 Smith Chart



Notes

1. As a result of the particularity of inner structure of SAW products, it easy to be breakdown by electrostatic, so we should pay attention to **ESD protect** in the test.
2. **Static voltage** between signal load and ground may cause deterioration and destruction of the component. Please avoid static voltage.
3. **Ultrasonic cleaning** may cause deterioration and destruction of the component. Please avoid ultrasonic cleaning.
4. Only leads of component may **be soldered**. Please avoid soldering another part of component.
5. There is a close relationship between the device's performance and **matching network**. The specifications of this device are based on the test circuit shown above. L and C values may change depending on board layout. Values shown are intended as a guide only.